

21. (3)

22. (1)

23. (2)

$$\text{As } R = \frac{V}{I} = \frac{W}{qI}$$

$$\therefore [R] = \frac{[ML^2T^{-2}]}{[AT][A]} = [ML^2T^{-3}A^{-2}]$$

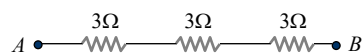
24. (2)

Current through each arm DAC and DBC = 1A

$$V_D - V_A = 2 \text{ and } V_D - V_B = 3 \Rightarrow V_A - V_B = +1V$$

25. (4)

The network can be redrawn as follows



$$\Rightarrow R_{eq} = 9\Omega$$

26. (1)

27. (2)

The circuit is a balanced Wheatstone bridge,

because

$$\frac{2\Omega}{2\Omega} = \frac{4\Omega}{4\Omega}$$

The 7Ω resistance is ineffective

$$\therefore \text{Resistance of the upper arms} = 2 + 2 = 4\Omega$$

$$\text{Resistance of the lower arms} = 4 + 4 = 8\Omega$$

These two resistances are in parallel

$$\therefore R_{AB} = \frac{4 \times 8}{4 + 8} = \frac{32}{12} = \frac{8}{3}\Omega$$

28. (4)

$$S = \left(\frac{100-1}{1} \right) R$$

$$\text{Initially, } 30 = \left(\frac{100-1}{1} \right) \times 10 \Rightarrow l = 25\text{cm}$$

$$\text{Finally, } 10 = \left(\frac{100-1}{1} \right) \times 30 \Rightarrow l = 75\text{cm} \text{ So, shift} \\ = 50\text{cm.}$$

29. (3)

$$\text{Here } \varepsilon = 3 \text{ V, } \varepsilon_1 = 1.08 \text{ V, } l_1 = 216 \text{ cm, } l = ?$$

$$\text{As } \frac{\varepsilon}{\varepsilon_1} = \frac{l}{l_1} \therefore l = \frac{\varepsilon}{\varepsilon_1} \times l_1 = \frac{3 \times 216}{1.08} = 600 \text{ cm}$$

30. (2)

Order of drift velocity

$$= 10^{-4} \text{ m/sec} = 10^{-2} \text{ cm/sec}$$